

Amendments to the Claims

This listing of claims will replace all prior versions and listings of claims in the above-identified application:

Listing of Claims

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Claim 1 (Currently Amended): A signal processing device, comprising:

inputting means for inputting a video signal;

encoding means for encoding to compress an amount of information of the video signal;

generating means for generating a character signal; and

control means for controlling a combination operation of the character signal;

and

memory means connected to each of said encoding means and said generating means and having a common memory for storing the video signal to perform a an encoding process by said encoding means and storing the character signal generated by said generating means using a predetermined table value corresponding to a control signal by said control means to perform a the combining operation of the character signal.

Claim 2 (Original): A device according to claim 1, further comprising:

combining means for combining the character signal generated by said generating means with the video signal.

Claim 3 (Previously presented): A device according to claim 2, wherein said encoding means encodes to compress an amount of information of the video signal outputted from said combining means.

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**Claim 4 (Currently Amended): A device according to claim 3, further comprising:**

**outputting means for outputting the video signal the amount of information of which has been encoded by said encoding means to a recording device, said recording device recording the vide video signal outputted from said outputting means on the recording medium.**

**Claim 5 (Original): A device according to claim 2, further comprising:**

**outputting means for outputting a video signal outputted from said combining means to a display device, said display device displaying an image represented by the video signal outputted from said outputting means.**

**Claim 6 (Original): A device according the claim 2, wherein said combining means combines the character signal with a video signal captured by the image pickup means.**

**Claim 7 (Currently Amended): A device according to claim 1, wherein said memory means has a first area for storing a video signal an amount of which is to be encoded by said encoding means, a second area for storing a video signal an amount of which has been encoded by said encoding means, and a third area which is different from said first area and said second area, said generating means generating the character signal by using the third area.**

**Claim 8 (Previously presented): A device according to claim 7, further comprising:**

**outputting means for reading out from said second area the video signal the amount of which has been encoded and outputting the read-out video signal to a recording**

device, said recording device recording the video signal outputted from said outputting means on a recording medium.

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**Claim 9 (Previously presented):** A device according to claim 1, wherein said memory means has a first area which is to be accessed by said encoding means, and a second area which corresponds to an image plane represented by the video signal and which is different from the first area, and wherein said generating means comprises memory control means for writing into said second area a plurality of codes representing a value of pixel data of the character signal, and a table for outputting pixel data corresponding to codes read out from said second area.

**Claim 10 (Original):** A device according to claim 9, further comprising:  
combining means for combining the character signal generated by said generating means with the video signal,

the codes representing control data for controlling a combining operation of said combining means, said combining means performing the combining operation in accordance with the codes.

**Claim 11 (Previously presented):** A device according to claim 1, wherein said encoding means comprises orthogonal transform means for orthogonally transforming the video signal, quantization means for quantizing orthogonal transform coefficients from said orthogonal transform means, and variable-length coding means for variable-length-coding an output of said quantization means.

**Claim 12 (Currently Amended):** A signal processing device, comprising:  
inputting means for inputting a compressed video signal;

expanding means for expanding an amount of information of the compressed video signal and outputting an expanded video signal;

generating means for generating a character signal; and

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control means for controlling the combination operation of the character signal; and  
memory means connected to each of said expanding means and said generating means and having a common memory for storing the compressed video signal to perform an expanding process by said expanding means and storing the character signal generated by said generating means using a predetermined table value corresponding to a control signal by said control means to perform a the combining operation of the character signal and the expanded video signal.

Claim 13 (Original): A device according to claim 12, further comprising:

combining means for combining the character signal generated by said generating means with the video signal.

Claim 14 (Canceled).

Claim 15 (Previously presented): A device according to claim 13, further comprising:

outputting means for outputting a video signal outputted from said combining means to a display device, said display device displaying an image represented by the expanded video signal outputted by said expanding means.

Claim 16 (Previously presented): A device according to claim 12, wherein said inputting means for inputting the compressed video signal reproduced from a recording medium by a reproduction device and writing the reproduced video signal into said memory

means, said expanding means expanding an amount of information of the compressed video signal written into said memory means by said inputting means.

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Claim 17 (Previously presented): A device according to claim 12, wherein said memory means has a first area for storing a compressed video signal an amount of which is to be expanded by said expanding means, a second area for storing a expanded video signal an amount of which has been expanded by said expanding means, and a third area which is different from said first area and said second area, said generating means generating the third character signal by using said third area.

Claim 18 (Previously presented): A device according to claim 12, wherein said memory means has a first area which is to be accessed by said expanding means, and a second area which corresponds to an image plane represented by the expanded video signal and which is different from said first area, and wherein said generating means comprises memory control means for writing into said second area a plurality of codes representing a value of pixel data of the character signal, and a table for outputting pixel data corresponding to codes read out from said second area.

Claim 19 (Previously presented): A device according to claim 18, further comprising:

combining means for combining the character signal generated by said generating means with the expanded video signal the amount of which has been expanded by said expanding means,

the codes indicating a combining operation of said combining means, said combining means performing the combining operation in accordance with the codes.

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Claim 20 (Currently Amended): A signal processing device, comprising:

inputting means for inputting a video signal;

processing means for performing a predetermined process on the video signal and outputting a processed video signal;

generating means for generating a character signal; and

control means for controlling a combination operation of the character signal;

and

memory means connected to each of said processing means and said generating means and having a common memory for storing the video signal and the processed video signal so as to perform the predetermined process by said processing means and storing the character signal ~~generated by said generating means~~ using a predetermined table value corresponding to a control signal by said control means to perform a the combining operation of the character signal.

Claim 21 (Original): A device according to claim 20, wherein said processing means includes a high-efficiency encoding means for compressing an amount of information of the video signal and for encoding the video signal.

Claim 22 (Previously presented): A device according to claim 21, wherein said memory means has a first area which is to be accessed by said high-efficiency encoding means, and a second area other than said first area, said generating means generating the character signal by using the second area.

Claim 23 (Original): A device according to claim 21, wherein said processing means further comprising error correction encoding means for error-correction-encoding the encoded video signal.

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Claim 24 (Previously presented): A device according to claim 23, wherein said memory means has a first area which is to be accessed by said high-efficiency encoding means, a second area which is to be accessed by said error correction encoding means, and a third area other than said first area and said second area, said generating means generating the character signal using said third area.

Claim 25 (Original): A device according to claim 20, wherein said processing means includes a high-efficiency decoding means for decoding the video signal and for expanding an amount of the decoded video signal.

Claim 26 (Previously presented): A device according to claim 25, wherein said memory means has a first area which is to be accessed by said high-efficiency decoding means, and a second area other than said first area, said generating means generating the character signal by using said second area.

Claim 27 (Original): A device according to claim 25, wherein said processing means further includes error-correction-decoding means for correcting any error in the video signal.

Claim 28 (Previously presented): A device according to claim 27, wherein said memory means includes a first area which is to be accessed by said high-efficiency decoding means, a second area which is to be accessed by said error correction decoding means, and a third area other than said first area and said second area, said generating means generating the character signal by using said third area.

Claim 29 (Currently Amended): A recording apparatus, comprising:

inputting means for inputting a video signal;

compressing means for compressing an amount of information of the video signal and outputting a compressed video signal;

recording means for recording on a recording medium the compressed video signal;

generating means for generating a character signal; and

control means for controlling a combination operation of the character signal;

and

memory means connected to each of said compressing means, said recording means and said generating means, and having a common memory for storing the video signal to perform a compressing process by said compressing means, the compressed video signal outputted from said compressing means to record on the recording medium by said recording means and the character signal ~~generated by said generating means~~ using a predetermined table value corresponding to a control signal by said control means to perform a the combining operation of the character signal.

Claim 30 (Original): An apparatus according to claim 29, further comprising;

combining means for combining the character signal with the video signal.

Claim 31 (Original): An apparatus according to claim 30, wherein said compressing means compresses an amount of information of a combined video signal outputted from said combining means, said recording means recording the combined video signal outputted from said compressing means.

Claim 32 (Original): An apparatus according to claim 30, further comprising:

display means for displaying an image represented by the combined video signal outputted from said combining means.

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Claim 33 (Previously presented): An apparatus according to claim 29, further comprising:

reproducing means for reproducing from the recording medium the compressed video signal compressed by said compressing means, and for writing the reproduced video signal into said memory means.

Claim 34 (Previously presented): An apparatus according to claim 33, further comprising:

expanding means for expanding an amount of information of the reproduced video signal by using said memory means; and

combining means for combining the character signal with the video signal the amount of which has been expanded by said expanding means.